

**RESPONSE UNDER 37 C.F.R. § 1.116  
EXPEDITING PROCEDURE  
EXAMINING GROUP 2800****Slater & Matsil, L.L.P.**Suite 1000  
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**FACSIMILE COVER SHEET**

To: Examiner Dana Farahani	Total Pages Sent: 13
Technology Center 2800	(including cover sheet)
Facsimile Number: 571-273-8300	Transmission Date: September 27, 2005

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Sheu, *et al.* Docket No.: TSM03-0140  
Serial No: 10/619,828 Art Unit: 2891  
Date Filed: July 15, 2003  
Title: Self-Aligned MOSFET having an Oxide Region below the Channel

**CERTIFICATION OF FACSIMILE TRANSMISSION**

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- Certification of Facsimile Transmission (1 page)
- Amendment Under 37 C.F.R. § 1.116 (6 pages)
- Copy of Applicants' prior Response dated May 16, 2005 (6 pages)

Respectfully submitted,

Judy A. Betts  
Legal Assistant**Confirmation Respectfully Requested**

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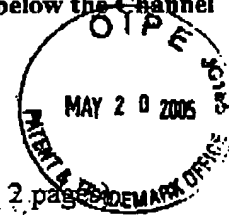
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The stamp of the USPTO, placed hereon, acknowledges receipt of:

Applicant: Sheu, *et al.* Docket No.: TSM03-0140  
Serial No: 10/619,828 Art Unit: 2829  
Filed: July 15, 2003 Date Mailed: May 16, 2005  
TITLE: Self-Aligned MOSFET having an Oxide Region below the Channel

Certificate of Mailing via First Class Mail (1 page)  
Response to Office Action (4 pages)  
Information Disclosure Statement (1 original and 1 copy = 2 pages)  
IDS Form PTO/SB/08a and 08b (6 pages) citing 52 references  
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MAY 25 2005

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant: Sheu, *et al.* Docket No.: TSM03-0140  
Serial No.: 10/619,828 Art Unit: 2829  
Filed: July 15, 2003 Examiner: Dana Farahani  
For: Self-Aligned MOSFET having an Oxide Region below the Channel

Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

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Respectfully submitted,

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